

Laminated and Framed on Board



UPCOMING ADVANCE TOPICS

Size: 20"X26"Laminated and Attached with Strips OR Size: 20"X26"Laminated and Framed on Board

Advance topics in Programming

- CH 2801 NET Technologies
- CH 2802 Java features
- CH 2803 MATLAB Fundamentals
- CH 2804 J2ME
- CH 2805 Compiler Design

Advance Topics in Database Technologies

- CH 2811 Big Data Concept
- CH 2812 Data Mining
- CH 2813 Data Warehousing
- CH 2814 ORACLE System
- CH 2815 MYSQL

Advance web Technologies

- CH 2821 LAMP Technologies
- CH 2822 Web Browsers
- CH 2823 E-Commerce
- CH 2824 PHP
- CH 2825 HTML Fundamentals
- CH 2826 Web Servers

Advance Computer Communication Technologies

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CH 2830	Working of Internet
CH 2831	Cluster Computing
CH 2832	Network Security
CH 2833	Stenography
CH 2834	Hacking and Attacking
CH 2835	Networking Simulator (NS2)
CH 2836	Distributed Computing
CH 2837	Mobile Computing

Latest Trends in Computing

- CH 2841 Neural Networks
- CH 2842 Fuzzy Logic Concepts
- CH 2843 Biometric System concepts
- CH 2844 Cloud computing
- CH 2845 Image Processing
- CH 2846 Open Source Technologies
- CH 2847 Android Operating Systems
- CH 2848 Natural Language Processing

Dbios COMPUTER/ IT Charts

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COMPUTER HARDWARE CHARTS

CH 2180	IDE INTERFACE	CH 2186	BACKUP POWER SUPPLY
CH 2181	HARD DISK DRIVE	CH 2187	PC MOTHERBOARDS
CH 2182	COMPUTER POWER SUPPLY	CH 2188	SCSI INTERFACE
CH 2183	CD ROM DRIVE	CH 2189	FLOPPY DISK DRIVE - ARCHITECTURE
CH 2184	PC PORTS & CONNECTORS		& WORKING
CH 2185	SEMI CONDUCTOR MEMORIES	CH 2190	PC KEYBOARDS
		CH 2191	PC MOUSE
		CH 2192	COMPUTER SYSTEM BUSES

BASIC COMPUTER

CH 2101	SUMMARY OF DOS COMMANDS
CH 2102	DOS COMMANDS
CH 2103	AUXILLARY STORAGE DEVICES
CH 2104	MEMORIES
CH 2108	INPUT DEVICES
CH 2109	OUTPUT DEVICES
CH 2125	TYPICAL TELNET & FTP COMMANDS
CH 2126	OOPS FEATURES

CH 2121 CH 2122 CH 2123 CH 2124 CH 2124 CH 2127 CH 2128	FUNDAMENTALS OF 'C'-I FUNDAMENTALS OF 'C'-II FUNDAMENTALS OF 'C'-III PROGRAMMING LANGUAGE C++ FUNDAMENTALS OF PATT-I
CH 2128	C++ FUNDAMENTALS OF PATT-II
CH 2129	C++ FUNDAMENTALS OF PATT-III

DATA BASE MANAGEMENT SYSTEM

CH 2114	DATA MODELS	CH 2134	NORMALISATIONS
CH 2131	DBMS ARCHITECTURE	CH 2135	RELATIONAL ALGEBRA
CH 2132	DATA BASE SYSTEM vs FILE SYSTEM	CH 2136	SQL COMMANDS
CH 2133	ENTITY RELATIONSHIP DIAGRAM		(STRUCTURED QUERRY LANGUAGE)

NETWORKING

CH 2105	OSI MODELS	CH 2111	TRANSMISSION MEDIAS
CH 2106	HUB/SWITCH	CH 2112	TYPES OF COMPUTER NETWORKS
CH 2107	NETWORK TOPOLOGIES	CH 2113	COMMUNICATION SWITCHING TECHNIQUES
CH 2110	MODES & FORMS OF DATA TRANSMISSION	CH 2141	IP-ADDRESS

A FEW PREVALENT SOFTWARES

CH 2115	MICROSOFT WORD	CH 2118	THE VISUAL BASIC IDE
CH 2116	MICROSOFT EXCEL	CH 2119	THE TOOL BARS OF VB
CH 2117	MICROSOFT POWER POINT		

SOFTWARE ENGG.

CH 2145 SOFTWARE LIFE CYCLE MODELS

CH 2146 SOFTWARE TESTING TECHNIQUES

DEADLOCK MANAGEMENT

CH 2155 PROCESS MANAGEMENT

OPERATING SYSTEM

0110454	OS COMPONENTS & FUNCTIONS
CH 2151	

- CH 2152 LINUX COMMANDS
- CH 2153 CPU SCHEDULING ALGORITHMS
 - **ARTIFICIAL INTELLIGENCE**

CH 2154

- CH 2156 MAJOR COMPONENTS OF ARTIFICIAL INTELLIGENCE
- CH 2157 **EXPERT SYSTEM ARCHITECTURE**
- CH 2158 MAJOR ROBOT COMPONENTS

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COMPUTER GRAPHICS

- CH 2161 GEOMETRIC TRANSFORMATIONS
- CH 2162 LINE DRAWING ALGORITHMS
- CH 2163 CIRCLE DRAWING ALGORITHMS
- CH 2164 ELLIPSE DRAWING ALGORITHMS
- CH 2165 PLANAR PROJECTIONS CH 2166 CLIPPING ALGORITHMS CH 2167 WINDOWING TRANSFORMATIONS

DATA STRUCTURES

	CH 2171	FUNDAMENTALS OF DATA STRUCTURES
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- CH 2172 STACK DATA STRUCTURE
- CH 2173 QUEUE DATA STRUCTURE
- CH 2174A LINKED LISTS-I

CH 2174B LINKED LISTS-II CH 2175 TREE DATA STRUCTURES CH 2176 SEARCHING TECHNIQUES CH 2177 TYPICAL SORTING TECHNIQUES

Dbios COMPUTER PIONEERS

Size: 12"X18"Laminated and Framed on Board

Size: 20"X26"Laminated and Framed on Board

PIONEERS

CONTRIBUTIONS

SCP 61 SCP 62 SCP 63 SCP 64 SCP 65 DCP 11 DCP 12 DCP 13 DCP 14 DCP 15 DCP 16 DCP 17 DCP 17 DCP 18 DCP 24 DCP 25 DCP 26 DCP 26 DCP 27 DCP 28 DCP 29 DCP 31 DCP 31 DCP 32 DCP 33 DCP 34 DCP 35 DCP 36 DCP 38 DCP 39 DCP 40 DCP 41 DCP 42 DCP 45 DCP 48 DCP 49 DCP 49 DCP 40 DCP 45	Charles Babbage Blasis Pascal Ada Augusta William (Bill) H. Gates Thomas J Watson John Cocke Douglas C. Engelbart Bob Frankston Carver Mead Ken Olsen Pickette Wayne D Dr. Vinton G. Cerf Robert Elliot Kahn Ken Thompson Paterson Tim Dennis Ritchie Bjarne Stroustrup James Gosling Brendan Eich Larry Ellison William (Bill) Coleman Michael Saul Dell Mr. Raj Saraf Azim Prem Ji N. R. Narayana Murthy Ratan Tata Shiv Nadar Flint Charles Ranlett Jason Allen Gordon E. Moore & Robert Noyce Andy Grove William Hewlett and David Packard Steve Jobs Steve wozniak Jerry Yang & David Filo Larry Page and Sergey Brin Sabeer Bhatia Grady Booch Ted Codd	Father of Computer First Mechanical Calculator The First programmer Founder of Microsoft Founder of IBM The concept of the Reduced Instruction Set Co In Developing the Mouse as a Input Device, Advancing the utility of Personal Computers Pioneering the automation, methodology and the For his introduction of the Minicomputer. Inventor of the Principle of CPU on Chip Father of internet Developed the TCP/IP The Unix Operating System, and for developm Developer of DOS Pioneered the C ++ Programming Language Pioneered the C++ Programming Language Creator of Java Script In Developing Oracle Pioneer of Symantec Founder of Dell Founder of Zenith Computers Founder of Infosysys Founder of Infosysys Founder of Infosysys Founder of INTEL Founder of Microsoft Founder of HP CEO & Founder of Apple Computers Co-Founder of Apple Computers Co-Founder of Apple Computers Co-Founder of Apple Computers Co-Founder Apple Computer Yahoo.com Google.com Hotmail.com	eaching of integrated circuit design. ent of the c Programming Language.
DCP 49	Grady Booch	Developing the Unified Modeling Language	the second s

COMPUTER PIONEERS

Dbios μ P CHARTS

Size: 30"X40"

on White Rexine with Plastic Roller :

MICRO PROCESSORS

- CH 1509 8086 PIN-LAYOUT & ARCHITECTURE
- CH 1545 8086 INSTRUCTION SET
- CH 1545A 8086 INSTRUCTION SET
- CH 1510 8085 BLOCK DIAGRAM
- CH 1511 8085 PIN LAYOUT & SIGNAL REPRESENTATION
- CH 1512 8085 SET INSTRUCTION

CH 1542 8051 BLOCK DIAGRAM

- CH 1550 8050 INTERRUPTS
- CH 1513 8255A THE PROGRAMMABLE PERIPHERAL INTERFACE
- CH 1514 8155 & 8755: THE PROGRAMMABLE DEVICE
- CH 1515 8279: THE PROGRAMMABLE KEYBOARD/DISPLAY INTERFACE
- CH 1516 8254 & 8259A: THE PROGRAMMABLE INTERNAL TIMER & INTERRUPT CONTROLLER

MICRO CONTROLERS

CH 1541 8051 ARCHITECTURE & PIN LAYOUT

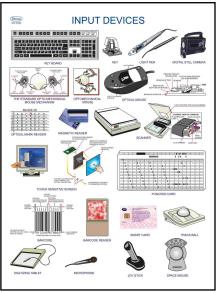
CH 1543 8051 INSTRUCTION SET

CH 1544 8051 SFR: SPECIAL FUNCTION REGISTERS

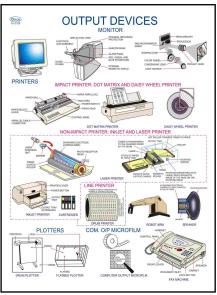
Dbios COMPUTER HISTORY CHARTS

Size: 20"X26"Laminated and attached with Strips Size: 20"X26"Laminated and Framed on Board

- DCH 01 Early Generation Computer
- DCH 02 First Generation Computer
- DCH 03 Second Generation Computer : Transistor DCH 04 Third Generation Computer : Post 1960's
- DCH 04 Inite Generation Computer : Post 19
- DCH 05 History of Internet



DCH 05



DCH 05



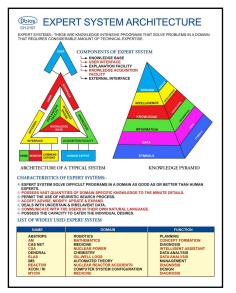
DCH 05

(Dbios)

COMPUTER PIONEERS







CH 2157



CH 2180



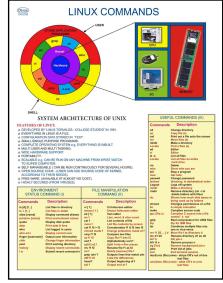
CH 2125



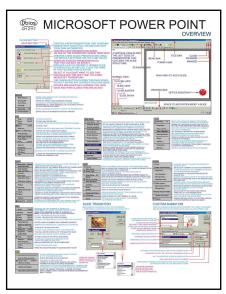
CH 2106



CH 2181



CH 2152



CH 2117



CH 2189

Dbios